

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.-11. (Canceled)

12. (Currently Amended) An isolation cell for isolating an embedded memory from other circuitry on an integrated circuit, comprising:

a totem pole arrangement of switches such that each drain and source terminal of the switches are connected to one of the group consisting of drain terminals of other switches in the totem pole arrangement, source terminals of other switches in the totem pole arrangement, power, or ~~and~~ ground, the totem pole arrangement of switches receiving a signal for an embedded memory, the totem pole arrangement of switches providing an output signal to the embedded memory; and

a latch circuit for latching the output signal from the totem pole arrangement of switches, the output signal being supplied to the embedded memory.

13. (Original) The isolation cell of Claim 12, the totem pole arrangement of switches including a switch controlled by a power level of core logic circuitry on the integrated circuit.

14. (Original) The isolation cell of Claim 13, the totem pole arrangement of switches further including a switch controlled by a power level of input and output circuitry on the integrated circuit.

15. (Original) The isolation cell of Claim 14, the totem pole arrangement of switches further including a switch controlled by a power level of analog circuitry on the integrated circuit.

16. (Original) The isolation cell of Claim 15, the totem pole arrangement of switches further comprising a switch controlled by a first enable signal and a switch controlled by a second enable signal.

17. (Original) The isolation cell of Claim 16, wherein the first and second enable signals are electrically connected to ground through pull down resistors.

18.-38. (Canceled)

REMARKS